PANDIT DEENDAYAL PETROLEUM UNIVERSITY, GANDHINAGAR

SCHOOL OF TECHNOLOGY

Department of Computer Science and Engineering

Minutes of the Board of Studies Meeting of CE and ICT programme

The first meeting of the Board of studies of the Computer Engineering and Information and Communication Technology (ICT) Programme of CSE Department was held on 10th March 2017 at 4.30 pm in E-Block Committee Room of SOT building PDPU. Prof. Tajinder Pal Singh, HOD of CE/ICT Department chaired the meeting. The following members were present in the meeting:

Dr. H.B. Raghavendra	Director, SOT.	
Dr. D. M. Parikh	Dean, FOET	
Dr. Tajinder Pal Singh	HOD, CSE Dept.	Chairman (BOS)
Computer Engineering		
Dr. Vibha Patel	Sr. Associate Professor, Nirma University	External Member
Dr. Asim Banerjee	Professor, DAIICT	External Member
Mr. Indrajit Mitra	Co-Founder, Jt. Managing Director, Gateway Technolabs	Professional Member
Mr. Harish Chib*	Vice President, Sophos Technologies Pvt Ltd.	Professional Member
Dr. Samir B. Patel	Assistant Professor, CE, PDPU	Internal Member
Dr. Manish Chaturvedi	Assistant Professor, CE, PDPU	Internal Member
Information and Communication Technology		
Dr. Darshak Thakore	Professor and Head, BVM.	External Member
Dr. Zunnun Narmawala	Associate Professor, Nirma University	External Member
Mr. Chirag Dhebar	Global Head, Global Consulting Practice, ERP, Transformation Group, TCS	Professional Member
Mr. Amit Rawat	Scientist SC, ISRO, Ahmedabad	Professional Member
Dr. Gangaprasad Pandey	Assistant Professor, ICT, PDPU	Internal Member
Dr. Jigar Shah	Assistant Professor, ICT, PDPU	Internal Member

Following other members were present in the meeting:
1) Dr. Nishant Doshi   -   Asst. Professor, Computer Engineering / ICT
2) Mrs. R. Jothi   -   Lecturer, Computer Engineering / ICT

*Mr. Harish Chib (Sophos Technologies Pvt Ltd) could not attend the meeting.

The following points were discussed in the meeting:

- The current curriculum has 25 conduct hours per week. Members of the BOS Committee suggested to increase the number of conduct hours per week and to relax the constraints in the course structure, so that few additional courses can be introduced. Addressing to this point, Director SOT replied that the

purpose of having 5 Theory + 2 Lab course structure is to make more free slots so that students get enough time for self-learning. However, the suggestion will be taken into consideration and the same will be discussed in Academic Council of PDPU for further action. However, the course structure may be adapted in a few semesters as required.

- Suggestions were given to make the 8th semester completely course free for Project and Industrial exposure. The same is under consideration at the University level.
- As per the University guideline, the maximum 10 % of teaching hours could be used for student centric learning. The topics for the same will be decided by the faculty members and will be communicated to the students at the beginning of the course. The course assessment will include such topics as well.
- The Question Papers of Mid Semester Examination and Semester End Examination will be reviewed by the committee formed by the department for quality assessment.
- Detailed syllabus of all the courses should follow the new format as per the University guidelines.
- The syllabus for lab courses is to be designed.
- The views given by the professional Members were deliberated and discussed at length. The course structure and content of the syllabus will take care of it over the four year period.
- Outline of the course structure was discussed and appropriate suggestions were given.
- The detailed syllabus of semester III and semester IV was reviewed and appropriate suggestions were given.
- The outline of the course structure and detailed syllabus of semester III and semester IV were approved with the following modifications:

Semester 3:
- Data Structures & Algorithms course (3-1-0) is to be changed to Data & File Structures (4-0-0). A few topics on advanced data Structures should be added in this course.
- The topics on sorting and searching algorithms are to be shifted to the Design & Analysis of Algorithms course.
- Data Structures & Algorithms Lab course (0-0-3) is to be renamed to Data & File Structures Lab (0-0-3).
- The courses of Communication Principles and Applications (3-1-0) of Semester 3 and Advanced Computer Networks (3-1-0) of Semester 5 are to be removed and relevant topics could be incorporated in Computer Networks (4-0-0) and Wireless Communication & Mobile Computing (3-1-0) Courses.
- Discrete Mathematics course (3-1-0) course is to be introduced in semester III.
- Database Management Systems Lab course (0-0-3) should be changed to DBMS Lab (0-0-3).

Semester 4:
- Computer Organization course (3-0-0) should be changed to Computer Organization & Programming course (3-1-0). This course will also include Assembly Language Programming.
- Microprocessor & Interfacing course (3-1-0) of Semester VI is to be removed.
- Object Oriented Concepts & Programming (3-0-0) should be renamed to Object Oriented Modeling & Design (3-0-0)
- Design & Analysis of Algorithms Lab (0-0-3) is to be removed. Instead, the Object Oriented Programming Lab (0-0-3) should be added.
- Computer Networks & ICT Workshop Lab (0-0-3) is to be changed to Computer Networks Lab (0-0-3).

Semester 5:
- The course credit of Operating Systems course is to be changed from (3-1-0) to (4-0-0). The topics related to the implementation of Operating Systems should be included.
- A subject related to Principles of Economics could be added in Semester 5.
- Artificial Intelligence (3-1-0) is to be added in Semester 5.
- Operating Systems Lab (0-03) should be introduced in placed of Advanced Computer Networks Lab (0-0-3).

Semester 6:
- Design of Operating System (3-1-0) course should be made Elective.
- System Software course (4-0-0) along with its lab could be introduced in place of Compiler Design course (3-1-0) of Semester VII. It may include topics related to design of assembler, linker, loader, etc. along with the topics on Compiler Design.
- Embedded Systems (3-0-0) is to be introduced in place of Microprocessor & Interfacing (3-1-0).
- Parallel & Distributed computing course (3-1-0) is to be shifted to Semester 7 with change in credit as (4-0-0). Accordingly, Parallel & Distributed Computing Lab (0-0-3) should be shifted to Semester 7.
- Wireless communication & Mobile Computing course (3-1-0) along with its lab could be shifted from semester 7 to Semester 6.
- System Software Lab (0-0-2), Embedded Systems Lab (0-02) and Web Technology Lab (0-0-2) may be introduced in place of Parallel and Distributed Computing Lab (0-0-3) and Web Technology Lab (0-0-3) in this semester.

Semester 7:
- A course on Information Security along with its lab should be added in Semester 7.
- It was suggested to make appropriate changes to the credit of Department Elective courses.

Semester 8:
- Data Mining course (2-2-0) is to be replaced with Big Data Analytics (4-0-0)

Further, the following points were discussed:
- The suggestions for restructuring of Elective courses in semester VII and VIII were given.
- It was suggested to have predominant part of subjects of Computer Engineering to be part of ICT
- Members of BOS suggested making the changes in ICT curriculum in similar lines of Computer Engineering. In this regard, separate discussions were carried out with some of the BOS members and the curriculum was finalized.

The meeting ended with a Vote of Thanks from Prof. T. P. Singh.

   Sd/-
Dr. T. P. Singh
Chairman BOS (CE and ICT)